

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
29 July 2004 (29.07.2004)

PCT

(10) International Publication Number
WO 2004/063910 A1

(51) International Patent Classification⁷: **G06F 1/00**

(21) International Application Number:
PCT/IB2003/006166

(22) International Filing Date:
15 December 2003 (15.12.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
03100033.4 10 January 2003 (10.01.2003) EP

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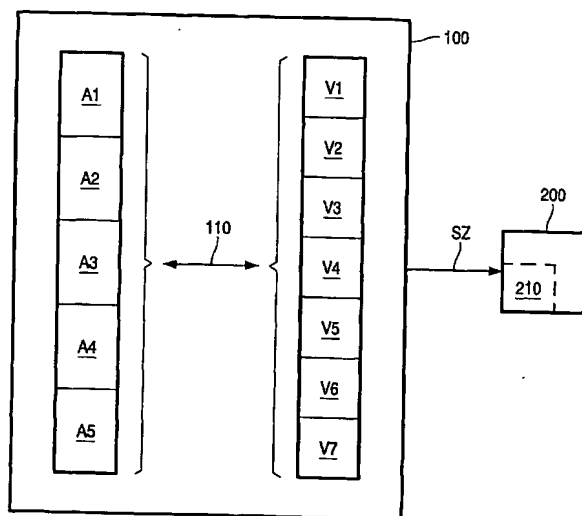
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(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),

[Continued on next page]

(54) Title: **CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING ELECTRONIC COMPONENTS AGAINST IL-
LICIT MANIPULATION**



(57) **Abstract:** To provide a microelectronic circuit arrangement (100) and a method for protecting at least one electronic component against illicit manipulation and/or unauthorized access, in which circuit arrangement (100) and method there are no opportunities for manipulation even during or in connection with the start-up procedure, it is proposed that there be arranged at least activating unit (A_i ; $i = 1, 2, 3, 4, 5$) for checking whether at least one activating condition is met and for activating at least one preventing unit (V_j ; $j = 1, 2, 3, 4, 5, 6, 7$) that is also associated with the circuit arrangement (100) and that is connected to the activating unit (A_i), by means of which preventing unit (V_j) the component (200) can be at least partly de-activated and/or at least partly destroyed in the event of illicit manipulation and/or unauthorized access.